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<u>L19</u>	L18 and (configur\$ near3 data)	10375	<u>L19</u>
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<u>L13</u>	L12 and ((configur\$ near3 data) near10 (operat\$ near3 configur\$))	37	<u>L13</u>
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<u>L9</u>	709/\$\$.ccls.	30520	<u>L9</u>
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<u>L7</u>	L6 and (configur\$ near3 data)	37	<u>L7</u>
<u>L6</u>	L5 and (identi\$ near5 device\$1)	54	<u>L6</u>
<u>L5</u>	L4 and (configur\$ near5 device\$1).ab.	99	<u>L5</u>
<u>L4</u>	L3 and (configur\$ near5 device\$1)	446	<u>L4</u>
<u>L3</u>	L2 or l1	1212	<u>L3</u>
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[Globus: A Metacomputing Infrastructure Toolkit - Foster \(1996\) \(Correct\) \(364 citations\)](#)

poses new challenges for developers of system **software**, parallel tools, and applications. In this Globus project is to address the problems of **configuration** and performance optimization in metacomputing the Globus project is to address the problems of **configuration** and performance optimization in elysium.cs.ucdavis.edu/~nico/seminar/papers/globus.ps.gz

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[Towards Programmable Networks - Yemini \(1996\) \(Correct\) \(82 citations\)](#)

Their functions, mostly implemented by embedded **software**, are rigidly built-in by intermediate nodes of elements has given rise to ever-complex **configuration** management. **Configuration** management has of a dynamically programmable networked **device**, and a language called NetScript for building brutus.snu.ac.kr/~hshin/seminar/.activenetwork9.ps

[The HP AutoRAID hierarchical storage system - Wilkes, al. \(1996\) \(Correct\) \(73 citations\)](#)

the HP AutoRAID technology is almost entirely in **software**, the additional hardware cost for these benefits this, RAID systems typically offer a great many **configuration** parameters: data- and parity-layout choice, this, RAID systems typically offer a great many **configuration** parameters: data- and parity-layout choice, www.hpl.hp.com/personal/Carl_Staelin/sosp95-tocs.ps.gz

[Programmable Active Memories: Reconfigurable Systems.. - Vuillemin, Bertin.. \(1996\) \(Correct\) \(71 citations\)](#)

offer a new mixture of hardware performance and **software** versatility. We review the important revolutionary component has a large internal **configuration** memory, and two modes of operation: in through the example of DECPerLe-1, an experimental **device** built in 1992. PAM programming is presented, in ftp.digital.com/pub/DEC/PRL/research-articles/VBRSTB941.ps.Z

[Context-Aware Computing Applications - Schilit, Adams, Want \(1994\) \(Correct\) \(80 citations\)](#)

and Roy Want Abstract This paper describes **software** that examines and reacts to an individual's over time and place. In short, the hardware **configuration** is continually changing. Similarly, the can promote and mediate people's interactions with **devices**, computers, and other people, and it can help ftp.parc.xerox.com/pub/schilit/wmc-94-schilit.ps

[A Component- and Message-Based Architectural Style.. - Taylor.. \(1996\) \(Correct\) \(37 citations\)](#)

and Message-Based Architectural Style for GUI **Software** Richard N. Taylor, Nenad Medvidovic, Kenneth M. style are components and connectors. A **configuration** of a system of components and connectors is components hooked together by message routing **devices**. Central to the architectural style is a sunset.usc.edu/~nenoteaching/s99/C2-TSE.ps.gz

[Adding Force Feedback to Graphics Systems: Issues and.. - Mark, Randolph.. \(1996\) \(Correct\) \(34 citations\)](#)

to these problems and describe our force-feedback **software** library which implements these techniques and to its task. It also allows for flexible system **configuration**, which is particularly useful in a research to its task. It also allows for flexible system **configuration**, which is particularly useful in a research www.cs.unc.edu/~billmark/SIG96withfonts.ps.Z

[A Dynamic Reconfiguration Run-Time System - Jim Burns \(1997\) \(Correct\) \(22 citations\)](#)

involved an ad hoc combination of hardware and **software**. The **software** that manages the dynamic specialised to one application and one hardware **configuration**. We present three different applications of specialised to one application and one hardware **configuration**. We present three different applications of www.dcs.ed.ac.uk/~adamd/research/fccm97.ps.gz

[Joust: A Platform for Liquid Software - John Hartman \(1998\) \(Correct\) \(15 citations\)](#)

Joust: A Platform for Liquid **Software** John H. Hartman, Larry L. Peterson, Andy and displays MPEG-compressed video. Such a **configuration** is specified at build time, and a set of its primary task is to move data from one **device** to another, for example, from a network **device** to

www.cs.arizona.edu/scout/Papers/joust.ps

Formal Requirements Analysis of an Avionics Control System - Dutertre, Stavridou (1997) (Correct) (18 citations)
 IEEE Transactions On **Software** Engineering, Vol. Xx, No. Y, Month 1997 1 Formal
 monitoring mechanisms and restores the initial **configuration** of the ADC (the primary channel active, the
 the wing sweep using an emergency mechanical **device**. The two channels are independent they have
www.csl.sri.com/~bruno/publis/safefm_tse.ps.gz

Structuring Communication Software for Quality-of-Service.. - Ashish Mehra (1996) (Correct) (23 citations)
 Structuring Communication **Software** for Quality-of-Service Guarantees Ashish Mehra,
 control over a Motorola 68040 CPU. This **configuration** avoids any interference from computation or
 complete control over a Motorola 68040 CPU. This **configuration** avoids any interference from computation or
rtcl.eecs.umich.edu/outgoing/atri/structuring.ps.Z

Landmark-Based Robot Navigation - Lazanas, Latombe (1992) (Correct) (33 citations)
 and algorithmic complexity in robotics. As **software** becomes more critical in modern robots, the
 area, an "island of perfection" in the robot **configuration** space where we consider position sensing and
 area, an "island of perfection" in the robot **configuration** space where we consider position sensing and
robotics.stanford.edu/~latombe/papers/algorithmica/landmark/paper.ps.gz

Self Modifying Circuitry - A Platform for Tractable Virtual.. - Adam Donlin (1998) (Correct) (9 citations)
 of applying virtual circuitry in a general **software** environment. Two primary limitations of existing
 bandwidth available to an array resident **configuration**, communication and computation agent. A
www.dcs.ed.ac.uk/~adamd/research/fpl98.ps.gz

The MR Toolkit Peers Package and Experiment - Shaw, Green (1993) (Correct) (24 citations)
 on some task in one common virtual space. **Software** that enables this kind of interaction must
 system is implemented in a local-area network **configuration**, and is susceptible to communications lags.
 process of an MR Toolkit application can transmit **device** data to other remote applications, and receive
menaik.cs.ualberta.ca/pub/graphics/papers/vrais.93.ps.gz

Model Checking Safety Critical Software with SPIN: .. - Cimatti.. (1997) (Correct) (11 citations)
 Model Checking Safety Critical **Software** with SPIN: an Application to a Railway
 verification with spin of significant process **configurations**. 1 Introduction This paper describes a joint
 verification with spin of significant process **configurations**. 1 Introduction This paper describes a
ftp.irisa.fr/pub/mirrors/netlib/spin/ws97/cimatti.ps.Z

Sensorless Manipulation Using Transverse Vibrations of .. - Bhatt.. (1996) (Correct) (16 citations)
 Although the vibratory motion is under **software** control, specialized mechanical nests must be
 proposition would result in finding the minimal **configuration** of resources required to solve a task. We
 proposition would result in finding the minimal **configuration** of resources required to solve a task. We
ftp.cs.cornell.edu/pub/brd/vibrating-plate.ps

The PARCTAB Mobile Computing System - Schilit, Adams, Gold, Tso, Want (1993) (Correct) (22 citations)
 in the near future. There are three types of **software** components in the PARCTAB system: gateways,
 before they can send data. A daisy-chained **configuration** is useful for long runs with low expected
 also use the network with other portable computing **devices**. 1 This work was supported by Xerox. Portions
ftp.parc.xerox.com/pub/schilit/wwos-93-schilit.ps.Z

Demultiplexed Architectures: A Solution for Efficient.. - Roca, Braun, Diot (1997) (Correct) (10 citations)
 of the workstations from the hardware and **software** points of view. The need to take advantage of
 to the communication subsystem. Therefore the **configuration** of a BSD stack is mainly hard-coded in kernel
 to the communication subsystem. Therefore the **configuration** of a BSD stack is mainly hard-coded in
ftp-sop.inria.fr/rodeo/diot/ieee-networks-us.ps.gz

An Automated Temporal Partitioning and Loop Fission .. - Kaul, Vemuri.. (1999) (Correct) (4 citations)
 are implemented in hardware and parts in **software**. The hardware targeted would be the
 at the start of the application, and the same **configuration** continues till the execution ends. However,
 associated with ASIC design is eliminated. But the **devic** capacity of FPGAs is far less than that of ASIC
www.eecs.uc.edu/~ddel/projects/sparcs/Papers/dac99.ps

StarT-Jr: A Parallel System from Commodity Technology - Hoe, Ehrlich (1996) (Correct) (12 citations)
 platforms with minimal loss in non-transferable **software** and hardware investment. On the other hand, FUNI
 Pentiumbased personal computers (PCs) in stock **configuration**, currently serve as the processing nodes to

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Page 3 of 3

Pentiumbased personal computers (PCs) in stock **configuration**, currently serve as the processing nodes to
www.csg.lcs.mit.edu/Users/jhoe/csgmemo/memo-384.ps.gz

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[Compiling Occam into Field-Programmable Gate Arrays - Page, Luk \(Correct\) \(34 citations\)](#)

hardware can be generated entirely by a **software** process. Inner loops of time-consuming programs Vendor-provided CAD **software** creates **device configuration** data from userprovided circuit schematics, innerloops, hardware interfaces to real-world **devices**, systolic arrays, and complete microprocessors. <ftp.comlab.ox.ac.uk/pub/Documents/techpapers/lan.Page/hwcomp.ps.gz>

[Neural Network Dynamics for Path Planning and Obstacle.. - Glasius, Komoda, Gielen \(1995\) \(Correct\) \(11 citations\)](#)

states are updated one at a time -implemented in **software** on a conventional computer is simply too slow conclusions. 2 The model 2.1 Work space and **configuration** space Consider a robot manipulator R, the conclusions. 2 The model 2.1 Work space and **configuration** space Consider a robot manipulator R, <ftp.mbfys.kun.nl/snn/pub/reports/Glasius.labyrinth.ps.Z>

[Implementing Fine-Grain Distributed Shared Memory On Commodity.. - Schoinas \(1996\) \(Correct\) \(11 citations\)](#)

fine-grain access control mechanisms (optimized **software**, commodity hardware, and custom hardware)a deadline!which we hope will permit larger **configurations**. 2 workstations running an unmodified Solaris 2.4 kernel, which it extends with loadable **device** drivers. Section 5 discusses this approach and <ftp.cs.wisc.edu/tech-reports/reports/96/tr1307.ps.Z>

[Allocating And Scheduling Hard Real-Time Tasks On A.. - Burns, Nicholson.. \(1994\) \(Correct\) \(12 citations\)](#)

in the design method MASCOT (Modular Approach to **Software** Construction, Operation and Test) can take on the issues of scheduling and allocation/**configuration** of a point-topoint parallel system, for external event (such as the arrival of data from a **device**) or an internal event (such as a clock tick or a <www.cs.york.ac.uk/ftpdireports/YCS-94-238.ps.Z>

[Workstation Video Playback Performance with Competitive Process.. - Kevin Fall \(1995\) \(Correct\) \(10 citations\)](#)

framing units, which are decoded by a **software** or hardware codec. The decoded frames are then in the presence of competitive load. The **configuration** under study consists of the three processes. in the presence of competitive load. The **configuration** under study consists of the three processes. <hulk.bu.edu/nossdav95/..nossdav95/papers/KevinFall.ps>

[Performance of Low-Cost UltraSparc Multiprocessors Connected by .. - Omang, Parady \(1996\) \(Correct\) \(10 citations\)](#)

(or sometimes even worse than) Ethernet. And ATM **software** overhead is still high. Since ATM is an The simplest back-to-back connected cluster **configuration** is pictured in figure 1. Connected to the I/O The simplest back-to-back connected cluster **configuration** is pictured in figure 1. Connected to the <www.ifi.uio.no/~sci/Publications/wmc97.ps>

[Formal Verification of a Power Controller Using the.. - Havelund, Larsen, Skou \(1999\) \(Correct\) \(3 citations\)](#)

processor interconnection and our model of the **software** architecture for one of the processors. Each rules. 2.1 Protocol Environment A typical B&O **configuration** consists of a number of components, which are rules. 2.1 Protocol Environment A typical B&O **configuration** consists of a number of components, which <ic-www.arc.nasa.gov/ic/projects/amphion/people/havelund/Publications/uppaal-power-arts99.ps.Z>

[An Operator Interface for Teleprogramming Employing Synthetic.. - Sayers, Paul \(1995\) \(Correct\) \(9 citations\)](#)

overview is also given of both the hardware and **software** used to implement the master station for the not be pressed at this time. ffl Current **configuration** -which keys are currently being activated master manipulator. This serves as both an input **device** (sensing where the operator wants to move) and an <www.cis.upenn.edu/~grasp/publications/sayers-presence94.ps.gz>

[Lag in Multiprocessor Virtual Reality - Wloka \(1995\) \(Correct\) \(9 citations\)](#)

of this synchronization scheme both by **software** simulation, as well as by actual lag using multiple processors in a pipelined **configuration** increases throughput yet maintains the same Packard, and Digital Equipment Corporation. **device**, various computation stages process and transform <wilma.cs.brown.edu/research/graphics/research/pub/papers/presence3.4-lag.ps>

The Design and Implementation of an IPv6 v4 Network Address... - Marc Fiuczynski (1998) (Correct) (4 citations)

use the new protocol will require changes to the **software** in every networked **device**. IPv4 systems, scalability and routing, security, ease-of-**configuration**, and higher performance compared to IPv4. require changes to the **software** in every networked **device**. IPv4 systems, however, are ubiquitous and are www.cs.washington.edu/homes/bershad/Papers/USENIX98/nap.ps

Network Subsystem Design: A Case for an Integrated Data... - Druschel, Abbott.. (1993) (Correct) (9 citations)

drivers, network protocols, and application **software** all potentially reside in different protection Company. protocol belongs in is delayed until **configuration** time (rather than OS design time) and, Company. protocol belongs in is delayed until **configuration** time (rather than OS design time) and, www.eecs.harvard.edu/cs248/papers/peterson-ieee93.ps

Domain Specific Mapping for Solving Graph Problems on... - Andreas Dandalis (1999) (Correct) (2 citations)

with the state-of-the-art. In comparison with **software** implementations, the estimated run-time speed-up of the hardware design or improvement of the **configuration** time. 1 Introduction Reconfigurable for Solving Graph Problems on Reconfigurable **Devices** Andreas Dandalis, Alessandro Mei and maarc.usc.edu/~pubs/andreas_raw99.ps

Probe-based Algorithm for QoS Specification and Adaptation - Nahrstedt, Hossain, Kang (1996) (Correct) (6 citations)

When a video card and its equivalent **software** is purchased, its description provides possible [Nah95]The QoS parameters are then stored in **configuration** files and retrieved when negotiation phase QoS specification is performed in two ways: ffl **Device** Specification When a video card and its berserk.vlsi.uiuc.edu/ftp/ipoint/Documents/qos.ps

An Evolved Circuit, Intrinsic in Silicon, Entwined With Physics... - Adrian Thompson (1996) (Correct) (6 citations)

in an on-chip memory, which can be written from **software** running on a host computer. No **configuration** of such application of evolution directly to the **configuration** of a Field Programmable Gate Array (FPGA) test version. A simplified representation of the **device** is shown in Fig. 1. It has E F S E S E N N F S W www.cogs.susx.ac.uk/users/adrianth/ices96/paper.ps.Z

Efficient Communication Mechanisms for Cluster Based... - Davis, Swanson, Parker (1996) (Correct) (6 citations)

(COWs)the largest latency component is the **software** code path between the application code and the communication in a multiprocessor cluster **configuration**. Workstation I/O architectures and networks forms, including CPU occupancy by protocol and **device** code as well as interference with CPU access to www.cs.utah.edu/~ald/camera-canpc.ps

Rolling Bodies with Regular Surface: Controllability Theory... - Marigo, Bicchi (1998) (Correct) (3 citations)

means of lifting complicity from hardware to the **software** and control level of design. In fact, planning mechanics. Consider a mechanical system whose **configurations** q evolve in a smooth n -dimensional manifold mechanics. Consider a mechanical system whose **configurations** q evolve in a smooth n -dimensional 131.114.28.35/pub/papers/roll-journal.ps.Z

"KhepOnTheWeb" : An Experimental Demonstrator in Telerobotics ... - Olivier Michel (1997) (Correct) (4 citations)

Web)the email box or the ftp servers full of **software**. Some experiments have been made to check the need of large-angle lenses. In its basic **configuration** (two lower layers of figure 1) Khepera is without need of large-angle lenses. In its basic **configuration** (two lower layers of figure 1) Khepera is lamiftp.epfl.ch/pub/michel/publications/VSM97.ps.gz

Remote I/O: Fast Access to Distant Storage - Foster, Kohr, Jr., Krishnaiyer, ... (1997) (Correct) (4 citations)

1 Introduction Improvements in networking and **software** infrastructure are making it easier for Nexus communication library to obtain access to **configuration** and security mechanisms provided by the Nexus communication library to obtain access to **configuration** and security mechanisms provided by the ftp.globus.org/pub/globus/papers/rio.ps.gz

Vb2 An Architecture For Interaction In Synthetic Worlds - Gobbetti (1993) (Correct) (8 citations)

Tools: Introduction and Survey. IEEE **Software**. 6(1)1523. 27] NSF (1992)Research communication (IPC)Figure 1 shows the typical **configurati** n of an immersive application. Processes are communication (IPC)Figure 1 shows the typical **configuration** of an immersive application. Processes are www.cs.umbc.edu/~gobbetti/publications/papers/uist93.ps

Dynamic Reconfiguration of Distributed Applications - Hofmeister (1993) (Correct) (8 citations)

written as distributed applications, where each **software** component (module) can execute on a different application is the act of changing the **configuration** of the application as it executes. Examples nenya.ms.mff.cuni.cz/thegroup/rel_work/3210.ps.gz

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